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METHOD FOR DETERMINING THE RECLOSE TIME OF A CIRCUIT BREAKER ON A HIGH VOLTAGE LINE

The present invention concerns a method for determining the reclose time of a circuit breaker on an electric network comprising a three-phase transmission line and a high voltage source. The method of the invention is more particularly adapted to extra high voltage lines, i.e. with a nominal source voltage of several hundred kV.

Such an electric network can be modelled according to a first approximation by the equivalent circuit shown figure 1. The network 1 comprises:

- a voltage source S,
- a three-phase transmission line L,
- a circuit breaker 6,
- a shunt compensation reactor 5,
- a capacitive voltage transformer 4.

The source voltage S is a very high voltage having a nominal value of $500~\rm{kV}$ for example and a network frequency of $50~\rm{Hz}$.

The three-phase transmission line L is a line of length 400 km for example enabling the transport of three phases A, B and C.

By circuit breaker is meant both a circuit breaker commanded by three independent single-pole commands each associated with a phase, and a circuit breaker commanded by a single three-pole command. Generally the three-phase circuit breaker 6 comprises at least three cut-off chambers each associated with one of phases A, B or C. If nominal voltage is high, several cut-off

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chambers may be connected in series. The circuit breaker 6 therefore comprises at least three pairs of contacts, each pair being associated with one of the three phases of line L and making it possible to interrupt any current circulating between source S and line L by separating the two contacts in the event of a fault on the associated phase, the first contact being on the source side and the second contact being on the line side. Only two contacts 7 and 8 associated with a phase of the circuit breaker 6 are shown in figure 1.

The shunt compensation reactor 5 is an inductance coil for example allowing compensation of capacitive reactive power on long, high-voltage, electric power transmission lines.

The capacitive voltage transformer 4 located at the start of the line is used to measure voltage on the line side of the circuit breaker.

A sudden change in configuration of the energy transport network generated by the functioning of a circuit breaker causes a rapid transient overvoltage, called switching surge which propagates over the network. These switching surges may occur on the tripping or reclosing of circuit breakers. Since the use of circuit breakers without re-enabling has become generalised (i.e. with automatic re-setting of the circuit breaker after tripping) it is on closing and especially on reclosing a line still containing a trapped charge that the highest overvoltages occur.

A first solution to this problem consists of using 30 an auxiliary system comprising a so-called closure resistance in series with a pair of auxiliary contacts,

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said auxiliary system being mounted in parallel with the cut-off chamber. The auxiliary contacts are actuated a few moments before the contacting of the main contacts so as to insert the closure resistance in the circuit. With this two-step triggering it is possible to reduce closure over-voltages with great efficacy.

This first solution, although very effective, has the drawback of being very costly.

A second solution consists of controlling the reclose times of the circuit breakers using electronic synchronization devices to replace the closure resistances. Said devices allow synchronized switching of a high voltage transmission line.

Therefore, when a single phase fault occurs (which account for more than 90% of line faults) on a high voltage line it is possible in certain networks that the fault involves elimination of three-pole opening of the circuit breaker followed by almost immediate re-closing (between 300 ms and 1s) in the endeavour to obtain ensured continuity of service. In this case, two of the phases are therefore vacuum switched by one of the end-of-line circuit breakers. On reclosing, the circuit breaker contacts must be closed at the right time (i.e. substantially at the time when the voltage at the contact terminals of the circuit breaker is zero) on these two vacuum phases, so as to limit overvoltages to an adequate value. This moment varies according to network configuration and must be determined by a closure algorithm in relation to the voltage signals measured on the network and supplied to

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the algorithm. The choice of right moment for reclosure is based on analysis of the voltage, at the contact terminals of the circuit breaker, of each healthy phase.

However, the implementing of this second solution also raises some difficulties.

lines compensated by For example the reactance have the characteristic, after opening, of oscillating at a frequency in the order of 50 to 90% of the network frequency. This oscillation frequency is its chiefly related to line capacity and compensation reactance. The voltage occurring on the breaker therefore terminals of the circuit greater or lesser beats depending upon the extent of compensation which varies in relation to transmitted power. During synchronization on reclosure, zero beat limit overvoltages. The be targeted to determination of this zero beat is not easy insofar as, in practice, the faulty phase influences the signal of the two healthy phases so that the signal obtained which is to be analysed at the contact terminals of the circuit breaker of the two healthy phases is of very complex shape and difficult to analyse. Consequently reach the desired accuracy may not results synchronization. Obtaining a satisfactory result i.e. at a determined confidence level, requires a relatively long convergence time for the closure algorithm used.

The present invention sets out to provide a method for determining the reclose time of a circuit breaker on an electric network, with which it is possible to improve the quality of the signals to be analysed and

thereby to reduce the convergence period of the algorithm used for determining the reclose time of the circuit breaker contacts after three-pole tripping due to the presence of a single-phase fault.

For this purpose the present invention proposes a method for determining the reclose time of a circuit breaker on an electric network, said network comprising:

- a high voltage source
- 10 a three-phase transmission line
 - a circuit beaker comprising at least three pairs of contacts, each pair being associated with one of the three phases of said line and enabling the interruption of any current circulating between said source and said line by the separation of said two contacts in the event of a fault on the associated phase, the first contact being on the source side and the second contact being on the line side,
- 20 a shunt compensation reactor to compensate capacitive reactive power on said line, said reclose time being determined in the event of

said reclose time being determined in the event of separation of the contacts of each pair of contacts in the presence of a fault on one of the three phases,

- 25 said determination of said reclose time being performed using the following steps:
 - measuring the voltage between the line side contact and earth, for each of the phases,
- measuring the voltage between the source side contact and earth, for at least one phase,
 - determining the voltage between the source side

contact and earth, for each of the phases,
said determination of said reclose time being
characterized in that it also comprises the following
steps:

- 5 calculating, for two separate phases called first and second phases, the voltage difference between the line side contact and earth for said first phase, and the voltage difference between the line side contact and earth for said second phase, the calculation being made for each pair of separate phases,
 - calculating the voltage difference between the source side contact and earth for said first phase and the voltage difference between the source side contact and earth for said second phase, the calculation being made for each pair of separate phases,
 - determining said reclose time on the basis of said voltage differences.

Under the invention, the algorithm for determining reclose time uses input data corresponding to the difference in signals between two phases on the source side and on the line side. With the method of the invention it is therefore possible in particular to calculate the voltage difference between two healthy phases on the line side and on the source side. The faulty phase influences the signals of the two healthy phases and, by using the difference in signals between two healthy phases, it is possible to eliminate the influence of the faulty phase. In this way a signal is recovered whose shape is much simpler to analyse. This

makes it possible to use algorithms for determining reclose time that have reduced convergence time for a given confidence level. It is necessary to calculate the difference for each pair of separate phases since it is not known in advance which phase is faulty.

Advantageously, said determination of the reclose time is made by comparing said voltage differences between two healthy phases on the line side and source side.

Advantageously, said determination of said reclose time is made by determining the instant at which the two signals corresponding to the said voltage differences between two healthy phases on the line side and source side are substantially equal and show the same monotony over a non-zero interval about said instant.

Advantageously, said determination of the voltage between the contact on the source side and earth for each of the phases is made by conducting 120° and 240° phase shifting of said voltage measured between the source side contact and earth for at least one phase.

Advantageously, said measurement of the voltage between the line side contact and earth for each phase is made using a voltage transformer.

Other characteristics and advantages of the present invention will become apparent in the following description of an embodiment of the invention given as an illustrative example and in no way restrictive.

In the following figures:

30 - figure 1 gives an equivalent circuit of an extrahigh voltage electric network

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- figure 2 schematically shows the three phases of an extra high voltage line such as the one in figure 1 on which the method of the invention is implemented,
- figure 3 shows two voltage signals obtained using the method of the invention.

In all the figures, the elements in common carry the same reference numbers.

Figure 1 has already been described in connection with the state of the art.

10 Figure 2 is a diagram of the three phases A, B and C of a high voltage line such as the line shown figure 1 on which the method of the invention is implemented.

The circuit breaker 6 comprises three pairs of contacts (7A, 8A), (7B, 8B) and (7C, 8C), each pair being associated with one of the three phases A, B or C and enabling the interruption of any current circulating between source S and line L through the separation of the two contacts in the event of a fault on the associated phase, the first contact being on the source side and the second contact being on the line side.

Let us assume that one of the phases A, B or C shows a fault; as a result circuit breaker 6 is tripped for the three phases A, B and C thereby separating the source side S from the line side L; it is said that the circuit breaker has three-pole tripping on a single-phase fault.

The objective of the method according to the invention is to provide the algorithm determining the reclose time of circuit breaker 6 with data signals that are easy to analyse. This algorithm must determine

a reclose time at which the voltages at the contact terminals of the circuit breaker are substantially zero. The algorithm used may for example be an algorithm using Prony's method, a neural recognition algorithm or a wavelet analysis algorithm such as described in the work "Wavelets and concurrent algorithms" (Y. Meyer, Hermann, 1992).

The method of the invention comprises a first step to measure the voltages UL_{A0} , UL_{B0} and UL_{C0} between the line side contacts of phases A, B and C and earth. These three measurements are made using three capacitive voltage transformers, not shown, each associated with one phase.

In parallel, the method also comprises a voltage 15 measurement step for at least one phase A, B or C between the source side contact of this phase and earth. Let US_{A0} be this voltage between the source side contact of phase A and earth.

The two other voltages US_{B0} and US_{C0} between the contacts for phases B and C and earth are deduced from voltage US_{A0} by 120° and 240° phase shifting of signal US_{A0} .

The method of the invention then comprises a calculation step of the six following differences:

These six differences are firstly the three

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voltage differences between two different phases on the source side, and secondly the three voltage differences between two different phases on the line side.

Let us assume that the initial fault concerns phase A.

Phase A, having a fault, influences the two healthy phases B and C. The differences $US_{B0}-US_{C0}=US_{BC}$ and $UL_{B0}-UL_{C0}=UL_{BC}$ are used to eliminate the effect of phase A on phases B and C, phase A acting in substantially identical manner on the two other healthy phases B and C. In this way two signals are obtained of much simpler form to analyse than signals obtained directly through the differences $US_{B0}-UL_{B0}$ and $US_{C0}-UL_{C0}$; said differences remain disturbed by the faulty phase and provide very difficult signals for subsequent analysis.

It is to be noted that it is necessary to calculate the six differences, and not only the differences related to the two healthy phases, since it is not known in advance which phase is the faulty phase.

The six differences corresponding to signals US_{AB} , US_{AC} , US_{BC} , UL_{AB} , UL_{AC} and UL_{BC} are then provided as input data for the determination algorithm which will determine the time of the closure command of circuit breaker 6.

Figure 3 therefore shows an example of voltage differences US_{BC} (= US_{B0} - US_{C0}) and UL_{BC} (= UL_{B0} - UL_{C0}) in relation to time in the event of a fault on phase A. Signals US_{BC} and UL_{BC} are near sinusoidal signals that are very scarcely disturbed and therefore relatively

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easy to analyse by the determination algorithm.

One manner of determining closure time T consists for example of taking position at a time when the two signals US_{BC} and UL_{BC} are substantially equal and show the same monotony over a non-zero interval about T. The two arrows shown in figure 3 give two examples of the choice of time T (in the first case the two signals US_{BC} and UL_{BC} increase about T, and in the second case the two signals US_{BC} and US_{BC}

Evidently the invention is not limited to the embodiment just described.

In particular the measuring, for each of the phases, of the voltage between the line side contact and earth has been described as being made using a capacitive voltage transformer but it may also be made using an inductive voltage transformer.